

Application No. 09/817,193

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

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1. (Currently Amended) A differential amplifier comprising:
  - a first differential amplifier circuit having a first differential pair and operating based on a common input voltage;
  - a second differential amplifier circuit having a second differential pair and operating based on the common input voltage;
  - a first current mirror circuit provided in the first differential amplifier circuit and formed from a first transistor of a primary conductive type and a second transistor of the primary conductive type;
  - a second current mirror circuit provided in the second differential amplifier circuit and formed from a first transistor of a secondary conductive type and a second transistor of the secondary conductive type;
  - a third transistor of the primary conductive type having a gate connected to a first output line of the first differential amplifier circuit; and
  - a third transistor of the secondary conductive type connected in series to the third transistor of the primary conductive type and having a gate connected to a second output line of the second differential amplifier circuit,
- wherein at least one of the first differential pair and the second differential pair is formed from a pair of transistors having a driving ability difference therebetween,
- and wherein a third output line connected between the third transistor of the primary conductive type and the third transistor of the second conductive type outputs an output voltage, and the first, the second and the third output lines are shorted

Application No. 09/817,193

together-together via a first passive device coupled to the first output line and a second passive device coupled to the second output line.

2. (Canceled)

3. (Previously Presented) The differential amplifier as defined in claim 1,  
wherein the first differential amplifier circuit includes:  
a fourth transistor of the secondary conductive type connected in series to the  
first transistor of the primary conductive type; and  
a fifth transistor of the secondary conductive type connected in series to the  
second transistor of the primary conductive type and having a driving ability different from  
the fourth transistor of the secondary conductive type,  
wherein the fourth transistor of the secondary conductive type and the fifth  
transistor of the secondary conductive type form the first differential pair.

4. (Original) The differential amplifier as defined in claim 3,  
wherein a driving ability of the fifth transistor of the secondary conductive  
type is set to be greater than a driving ability of the fourth transistor of the secondary  
conductive type.

5. (Previously Presented) The differential amplifier as defined in claim 1,  
wherein the second differential amplifier circuit includes:  
a fourth transistor of the primary conductive type connected in series to the  
first transistor of the secondary conductive type; and  
a fifth transistor of the primary conductive type connected in series to the  
second transistor of the secondary conductive type and having a driving ability different from  
the fourth transistor of the primary conductive type,  
wherein the fourth transistor of the primary conductive type and the fifth  
transistor of the primary conductive type form the second differential pair.

Application No. 09/817,193

6. (Original) The differential amplifier as defined in claim 5,  
wherein a driving ability of the fifth transistor of the primary conductive type  
is set to be greater than a driving ability of the fourth transistor of the primary conductive  
type.

7. (Original) A semiconductor device comprising the differential amplifier as  
defined in claim 1.

8. (Original) A power supply circuit comprising the differential amplifier as  
defined in claim 1.

9. (Original) Electronic equipment comprising the power supply circuit as  
defined in claim 8.

10. (Previously Presented) The differential amplifier as defined in claim 3,  
wherein the common input voltage is applied to a gate of the fourth transistor  
of the secondary conductive type,

and wherein the first output line is connected to a gate of the fifth transistor of  
the secondary conductive type through a first oscillation prevention capacitor and to the third  
output line through the first oscillation prevention capacitor and a first static electricity  
protection resistor.

11. (Previously Presented) The differential amplifier as defined in claim 5,  
wherein the common input voltage is applied to a gate of the fourth transistor  
of the primary conductive type,

and wherein the second output line is connected to a gate of the fifth transistor  
of the primary conductive type through a second oscillation prevention capacitor and to the  
third output line through the second oscillation prevention capacitor and a second static  
electricity protection resistor.